## LISTINGS OF THE CLAIMS

1. (Currently Amended) A communication control device that controls data communication between a host computer and a peripheral device, comprising:

first buffer memory that stores data to be sent to the host computer;

second buffer memory that stores data outputted from the peripheral device; and

a control unit that transfers the data stored in the second buffer memory to the first buffer memory, when receiving a transmission approval command of approving data transmission from the peripheral device to the host computer,

wherein the first buffer memory and the second buffer memory are FIFO buffers.

2. (Cancelled)

3. (Original) The communication control device according to claim 1, wherein the second buffer memory is a multi-stage FIFO buffer.

4. (Original) The communication control device according to claim 1, wherein the data communication is performed using a USB line.